

COPIE JUN 07 2004
PATENT & TRADEMARK OFFICE

BUR920040001US1 (WCR)
J.M. COHN, et al.

1/2

FIG. 1
LOGIC CONES

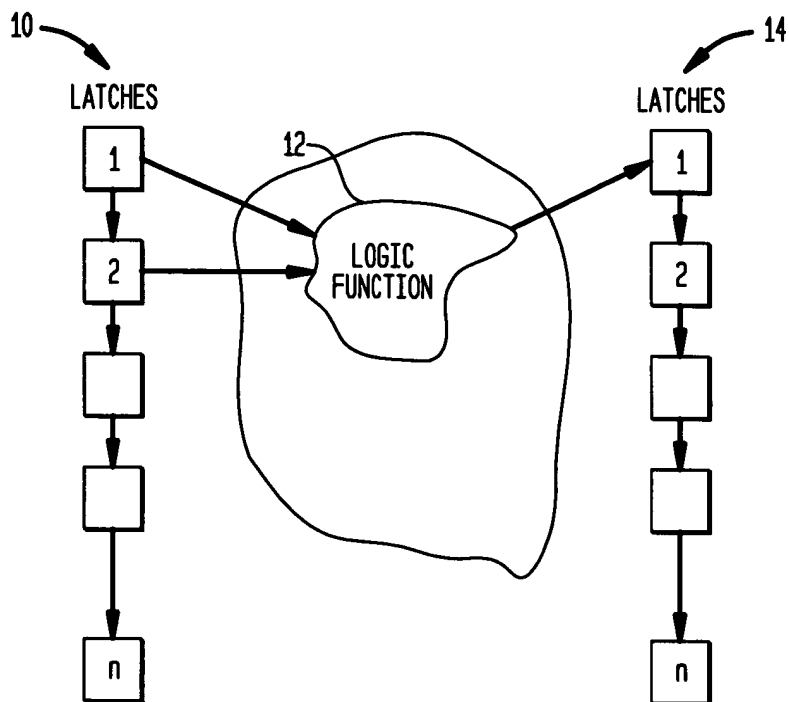


FIG. 2
STRUCTURE

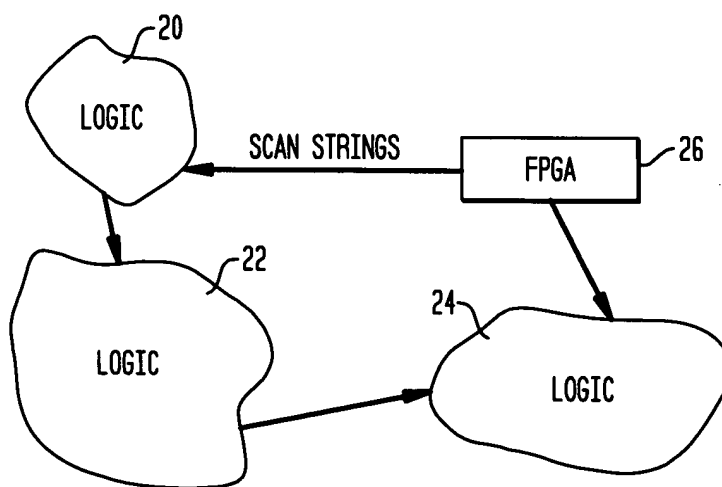


FIG. 3
FUNCTION TO FPGA

